



codesign virtual machine UML OR XML

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A codesign virtual machine for hierarchical, balanced hardware/software system modeling

JAM Paul, SN Peffers, DE Thomas - PROC DES AUTOM CONF. pp. 390-395. 2000, 2000 - portal.acm.org

... a high system level to the **machine** implementation. ... Previous hardware-software **codesign** approaches do not ... unrestricted software and hardware **virtual** machines in ...Cited by 7 - [Web Search](#) - [sslabs.cs.nthu.edu.tw](#) - [ece.cmu.edu](#) - [sigda.org](#) - [all 11 versions »](#)

[book] Hardware-software co-design of embedded systems: The Polis approach

F Balarin, M Chiodo, P Giusto, H Hsieh, A Jurecska ... - 1997 - Kluwer International Series in Engineering and Computer

Cited by 370 - [Web Search](#) - [Library Search](#)

Java Virtual Machine Specification, 2nd edition

T Lindholm, F Yellin - 1999 - portal.acm.org

... of the tenth international symposium on Hardware/software **codesign**, May 06 ...Bucka-Lassen, Nosa Omorogbe, The architecture of a **UML virtual machine**, ACM SIGPLAN ...[Web Search](#)

[book] Text Compression

TC Bell, JG Cleary, IH Witten - 1989 - Prentice Hall

Cited by 677 - [Web Search](#) - [Library Search](#)

[book] Genetic Algorithms in Search, Optimization and Machine Learning, 1st edition

DE Goldberg - 1989 - Addison-Wesley Longman Publishing Co., Inc. Boston, MA, USA

Cited by 10186 - [Web Search](#) - [Library Search](#)

[ps] Heterogeneous Development of Hybrid Systems

I Peter, A Pretschner, T Stauner - Proc. GI workshop Rigorose Entwicklung software-intensiver ..., 2000 - inf.ethz.ch

... 10], a hybrid variant of **UML's** Sequence ... However, unlike hardware/software **codesign**the partitioning into ... The possibility to create **virtual machine** models of ...Cited by 5 - [View as HTML](#) - [Web Search](#) - [autofocus.in.tum.de](#) - [autofocus.informatik.tu-muenchen.de](#) - [inf.ethz.ch](#) - [all 9 versions »](#)

A code generation framework for Java component-based designs

J Tsay, C Hylands, E Lee - CASES, 2000 - portal.acm.org

... Figure 3. The Visitor pattern in **UML**. ... version, for execution on a much smaller **virtual****machine**. ... Processors," in Hardware Software **Codesign**, Kluwer Academic ...Cited by 13 - [Web Search](#) - [hwswworld.com](#) - [gigascale.org](#) - [ptolemy.eecs.berkeley.edu](#) - [all 6 versions »](#)

The Java programming language (2nd ed.)

K Arnold, J Gosling - Addison Wesley Java Series, 1998 - portal.acm.org

... Omorogbe, The architecture of a **UML virtual machine**, ACM SIGPLAN ... isolation in theJava **Virtual Machine**, ACM SIGPLAN ... workshop on Hardware/software **codesign**, p.15 ...[Web Search](#) - [portal.acm.org](#)

Logic Synthesis and Verification Algorithms, 1st edition

GD Hachtel, F Somenzi - 2000 - portal.acm.org

... Damian Wells, The HSSM macro-architecture, **Virtual Machine** and H ... System-on-chipvalidation using **UML** and CWL ... conference on Hardware/software **codesign** and system ...[Web Search](#)

Reengineering legacy embedded systems

VK Madiseti, YK Jung, MH Khan, J Kim, T Finnessy - IEEE Design & Test of Computers, 1999 - ieeeexplore.ieee.org

... to develop an executable **virtual** prototype of the legacy system in a languagesuch as VHDL (VHSIC 1-hardware Description Language) or **UML** (Unified Modeling ...

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S1	1	5867400[pn]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/26 17:12
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S58	6	(UML XML) and co\$1design and (virtual with machine)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/02 11:21